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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/664,048      | 09/15/2003  | Barrie Gilbert       | 1482-176            | 2584             |

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EXAMINER

CHOE, HENRY

ART UNIT PAPER NUMBER

2817

DATE MAILED: 04/11/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/664,048

Applicant(s)

GILBERT ET AL

Examiner

Henry K. Choe

Art Unit

2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 03 March 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) 2, 6-12 and 15-19 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1, 3-5, 13 and 14 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 11 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)  | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>3/3/05</u> . | 6) <input type="checkbox"/> Other: _____  |

## DETAILED ACTION

### *Response to Election*

Applicant provisionally elected the species I without traverse. Applicant also designated that the claims 1, 3-5, 13 and 14 read on species I. Examiner agrees with this statement. Therefore, it is concluded that the claims 2, 6-12 and 15-19 are considered non-elected claims.

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 3-5, 13 and 14 are rejected under 35 U.S.C. 102(b) as being anticipated by applicant's admitted prior art (Fig. 3).

Regarding claims 1, 4, 13 and 14, applicant's admitted prior art (Fig. 3) discloses an amplifier circuit comprising an input terminal (a terminal receiving –IN signal, a terminal receiving +IN signal), a first gain stage (Q1, Q2) includes a pair of input transistors, the operational amplifiers A1 and A2 include a second gain stage and an output stage to provide inverting (a signal coming out of a collector terminal of transistor Q1) and non-inverting (a signal coming out of a collector terminal of transistor Q2) differential output signals, and a feedback signal (R57, R56) which are electrically connected between the inverting (a collector terminal of transistor Q1) and non-inverting (a collector terminal of transistor Q2) output nodes to emitters (emitters of transistors Q1 and Q2) of the input transistors (Q1, Q2) through a resistor network (R57, R56).

Regarding claim 3, the amplifier further including a feedback resistor (R57) which are electrically connected between the inverting node (a collector terminal of transistor Q1) and the input terminal (a terminal receiving -IN signal).

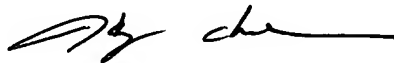
Regarding claim 5, the first gain stage (Q1, Q2) further including a pair of transistors (Q1, Q2) having emitters (emitters of Q1 and Q2) electrically connected to the output node (a collector terminal of transistor Q1, a collector terminal of transistor Q2) through a feedback resistor network (R57, R56).

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent numbers (5,587,674; 5,537,079) are the differential amplifiers with the feedbacks.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry Choe whose telephone number is (571) 272-1760.



**HENRY CHOE  
PRIMARY EXAMINER**

#995